

Patent

Attorney Docket No.: 2207/11273

CZC

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANTS : Thomas D. FLETCHER  
SERIAL NO. : 09/893,871  
FILED : June 29, 2001  
PATENT NO. : 6,928,572 B2  
ISSUED : August 9, 2005  
FOR : MULTISTAGE CLOCK DELAY CIRCUIT AND METHOD  
GROUP ART UNIT : 2116  
EXAMINER : Thuan Du

*Certificate  
JAN 06 2006  
of Correction*

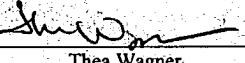
M/S: CERTIFICATE OF  
CORRECTION BRANCH  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**Certificate of Mailing**

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Date: December 27, 2005

Signature

  
Thea Wagner

**REQUEST FOR CERTIFICATE OF CORRECTION**

Dear Sir:

We have compared the above patent with the application as filed and have found errors in the printing of the patent. We respectfully request that the enclosed Certificate of Correction on Form PTO-1050 be issued correcting the mistakes set forth therein under authority of 35 U.S.C. §254. The exact column and line number where the errors occurred in the patent are listed on the enclosed certificate.

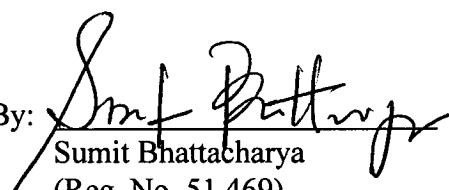
The errors that appear in the patent are typographical errors made by the Patent Office and therefore, no fee is required.

The Office is hereby authorized to charge any additional fees, or credit any overpayments, to Deposit Account No. **11-0600**.

Respectfully submitted,

KENYON & KENYON

By:

  
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Dated: December 27, 2005

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UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION

PATENT NO : 6,928,572 B2

DATED : August 9, 2005

INVENTOR(S) : Thomas D. FLETCHER et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 9, line 52, "to signal at the a clock"  
should be --to the signal at a clock--;

Column 10, line 57, "delay block provide"  
should be --delay block to provide--.

MAILING ADDRESS OF SENDER: Attorney Ref.: 2207/11273  
KENYON & KENYON

PATENT NO. 6,928,572 B2

Date: Dec. 27, 2005 333 W. San Carlos, Ste 600  
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